Liquid Crystal Display Device Having Patterned Electrodes for Repetitive Divided Horizontal Electric Field and Fringing Electric Field

4-26-2011

Shin-Tson Wu  
*University of Central Florida*

Zhibing Ge  
*University of Central Florida*

Xinzhang Wu  
*University of Central Florida*

Xinyu Zhu  
*University of Central Florida*

Find similar works at: [http://stars.library.ucf.edu/patents](http://stars.library.ucf.edu/patents)

University of Central Florida Libraries [http://library.ucf.edu](http://library.ucf.edu)

Recommended Citation

Wu, Shin-Tson; Ge, Zhibing; Wu, Xinzhang; and Zhu, Xinyu, "Liquid Crystal Display Device Having Patterned Electrodes for Repetitive Divided Horizontal Electric Field and Fringing Electric Field" (2011). *UCF Patents*. Paper 302.  
[http://stars.library.ucf.edu/patents/302](http://stars.library.ucf.edu/patents/302)

This Patent is brought to you for free and open access by the Technology Transfer at STARS. It has been accepted for inclusion in UCF Patents by an authorized administrator of STARS. For more information, please contact lee.dotson@ucf.edu.
LIQUID CRYSTAL DISPLAY DEVICE HAVING PATTERNED ELECTRODES FOR REPETITIVE DIVIDED HORIZONTAL ELECTRIC FIELD AND FRINGING ELECTRIC FIELD

Inventors: Zhibing Ge, Orlando, FL (US); Xinyu Zhu, Orlando, FL (US); Shin-Tson Wu, Oviedo, FL (US); Thomas Xinzhang Wu, Oviedo, FL (US)

Assignees: University of Central Florida Research Foundation, Inc., Orlando, FL (US); Toppoly Optoelectronics Corp., Miao-Li County (TW)

Apparatus, methods, systems and devices for high aperture ratio, high transmittance, and wide viewing angle liquid crystal display having first and second substrates each with an alignment layer and polarizer on the interior and exterior surface thereof and a liquid crystal material therebetween forming plural pixels each having a common electrode group and a pixel electrode group each having at least one common and pixel electrode. A fringe field drives the molecules in the regions above and below the electrodes and a horizontal field drives the molecules between the electrode groups to achieve high transmittance. In an embodiment an insulating layer separates the substrate and alignment layer and the pixel electrodes are on the substrate.

9 Claims, 21 Drawing Sheets
OTHER PUBLICATIONS


* cited by examiner
Fig. 1a
Prior Art

![Graph showing transmittance (%) vs. position (µm)]

Fig. 1b
Fig. 2c
Fig. 3c

Transmission

Structure

401 402 404 405
Fig. 4c
Fig. 7c
Fig. 8a
Fig. 8b
Fig. 9a

Prior Art
Fig. 9b

Prior Art
Fig. 9b

Fig. 9c
LIQUID CRYSTAL DISPLAY DEVICE HAVING PATTERNED ELECTRODES FOR REPEETITIVE DIVIDED HORIZONTAL ELECTRIC FIELD AND FRINGING ELECTRIC FIELD

FIELD OF THE INVENTION

The present invention is related to liquid crystal displays, and more particularly to a apparatus, methods, systems and devices for a high aperture ratio, high transmittance, and wide viewing angle liquid crystal display device driven by groups of electrodes in each pixel area that is useful for high-definition televisions and monitors which require high brightness and large viewing angle performance.

BACKGROUND AND PRIOR ART

Generally, based on the on-state orientation scheme of liquid crystal major axis, the direction of the axis is referred to as directors, there are two primary types of liquid crystal displays LCDs. The first type of LCD has directors that rotate mainly in a plane orthogonal to a substrate and the second type has directors rotating mainly in a plane parallel to the substrate.

The most typical mode of the first category is the twisted nematic liquid crystal display TN LCDs, in which the electrodes are configured on both of the two substrates. The TN LCD is usually arranged to be twisted at an angle of 90 degrees and shows high transmittance, low power consumption, and easy fabrication. However, the TN LCD exhibits a narrow viewing angle because its liquid crystal directors on the two substrate surfaces are perpendicularly aligned to each other.

Various methods have been disclosed to solve the problem of the narrow viewing angle in typical LCD devices. One effective way of solving the problem is the introduction of multi-domain structures in the LCD devices. However, multi-domain in TN type LCDs such as the two or four-domain TN LCDs are limited in improving the viewing angle to achieve a contrast ratio approximately >10:1 confined in a viewing cone at approximately 35 degrees.

Film compensation is another effective method to enhance the viewing angle of TN-LCDs. The major breakthrough was made by Fuji Photo Film Co., which used discotic liquid crystals as a wide viewing film to compensate the dark state of TN LCDs as described in Mori et al, Late-News Paper: Novel Optical Compensation Film for AMLCDs Using Discotic Compound, SID (1997), pp. 941-944. The viewing angles of TN LCD with wide viewing film could be expanded the 10:1 contrast ratio cone to approximately 60 degrees. However, a 60-degree viewing angle is inadequate for large LCD TVs and monitors, especially for solving the problem of non-uniform gray scale at larger viewing angle.

Other display modes for the twisted nematic LCDs, in which the liquid crystal directors mainly rotate in the plane perpendicular to the substrates, for solving the wide-viewing angle problem include the film-compensated multi-domain vertical alignment described in Takeda et al, A Super-High Image Quality Multi-Domain Vertical Alignment LCD by New Rubbing-Less Technology, SID (1998) p. 1077, and patterned vertical alignment mode described in Kim et al, Domain Divided Vertical Alignment Mode with Optimized Fringe Field Effect, ASID (1998) p. 383 liquid crystal display mode. Each has advantages and disadvantages, however problems still exist. Usually negative liquid crystal materials are preferred in the modes just described and they require complex fabrication methods such as protrusion in multi-domain vertical alignment.

SUMMARY OF THE INVENTION

A primary objective of the invention is to provide new methods, systems, apparatus and devices for a liquid crystal display device in which the electrodes are designed to provide a high aperture ratio, high transmittance and a wide viewing angle.

A secondary objective of the invention is to provide new methods, systems, apparatus and devices for a liquid crystal display device that has the wide viewing angle of the in-plane switching mode LCD with high transmittance comparable to the twisted nematic liquid crystal displays.

A third objective of the invention is to provide new methods, systems, apparatus and devices for maintaining a high storage capacitance by including an additional pixel auxiliary electrode that occupies a small ratio of the transparent area to increase the aperture ratio and transmittance of the liquid crystal display.

A liquid crystal display including a first and second substrate having plural pixels defined therebetween. The first and second substrates have polarizing layer on an exterior surface and an alignment layer on an inner surface and a liquid crystal layer sandwiched between the first and second alignment
layer, the liquid crystal layer treated so that liquid crystal molecules are aligned parallel to the surfaces of the first and second substrate in the absence of applied electric fields. Each of the plural pixels includes a first and second group of electrodes each having at least one common electrode and at least one pixel electrode and a thin-film transistor formed on each pixel to generate a substantially fringing electric field containing both horizontal and vertical electric field components within a region above and below the first group of electrodes and a substantially fringing electric field containing both horizontal and vertical electric field components is generated within the regions above and below the second group of electrodes and a substantially horizontal electric field is generated within the regions of the liquid crystal layer between the first and second group electrodes.

In an embodiment, the liquid crystal display includes an insulating layer between the substrate and the alignment layer and the common electrodes are on the substrate and the pixel electrodes are on the insulating layer. In yet another embodiment, at least one phase retardation film is layered between one of the substrates and the corresponding one of the polarizing layers to improve the viewing angle.

Further objects and advantages of this invention will be apparent from the following detailed description of preferred embodiments which are illustrated schematically in the accompanying drawings.

**BRIEF DESCRIPTION OF THE FIGURES**

FIG. 1A is a schematic of a prior art in-plane switching LCD structure with opaque electrodes and the transmittance curve versus position under crossed-polarizers corresponding to the in-plane switching LCD structure.

FIG. 1B is a schematic of another prior art in-plane switching LCD structure with transparent electrodes and a graph of the transmittance curve versus position under crossed-polarizers corresponding to the in-plane switching LCD.

FIG. 2A is a schematic of a high transmittance and wide viewing angle LCD structure according to the present invention with common and pixel electrodes located in the same layer.

FIG. 2B is a graph of the simulated transmittance curve versus position under crossed-polarizers of the high transmittance and wide viewing angle LCD structure shown in FIG. 2A with a positive dielectric anisotropic, homogeneous liquid crystal cell.

FIG. 2C is another graph of the simulated transmittance curve versus position under crossed-polarizers of the high transmittance and wide viewing angle LCD structure shown in FIG. 2A with a negative dielectric anisotropic, homogeneous liquid crystal cell.

FIG. 3A is a schematic of a high transmittance and wide viewing angle LCD structure with common and pixel electrodes located in two different layers.

FIG. 3B is a graph of the simulated transmittance curve versus position under crossed-polarizers of the high transmittance and wide viewing angle LCD structure shown in FIG. 3A with a positive dielectric anisotropic, homogeneous liquid crystal cell.

FIG. 3C is a graph of the simulated transmittance curve versus position under crossed-polarizers of the high transmittance and wide viewing angle LCD structure shown in FIG. 3A with a negative dielectric anisotropic, homogeneous liquid crystal cell.

FIG. 4A is a schematic of a high transmittance and wide viewing angle LCD structure with both common electrodes and pixel electrodes located in different layers.

FIG. 4B is a graph of the simulated transmittance curve versus position under crossed-polarizers of the high transmittance and wide viewing angle LCD structure shown in FIG. 4A with a positive dielectric anisotropic, homogeneous liquid crystal cell.

FIG. 4C is the simulated transmittance curve versus position under crossed-polarizers of the high transmittance and wide viewing angle LCD structure shown in FIG. 4A with a negative dielectric anisotropic, homogeneous liquid crystal cell.

FIG. 4D is a graph of the simulated transmittance curve versus position under crossed-polarizers of the high transmittance and wide viewing angle LCD structure shown in FIG. 4A with a negative dielectric anisotropic, homogeneous liquid crystal cell.

FIG. 4E is a graph of the simulated transmittance curve versus position under crossed-polarizers of the high transmittance and wide viewing angle LCD structure shown in FIG. 4A with a negative dielectric anisotropic, homogeneous liquid crystal cell.

FIG. 5A is a graphical representation of the simulated color shift corresponding to the structure shown in FIG. 4A with positive dielectric anisotropic liquid crystals for the PI cell shown in the solid line and the homogeneous liquid crystal cell represented by circles.

FIG. 5B is a graphical representation of the simulated color shift of the structure as FIG. 4A with negative dielectric anisotropic liquid crystals for the PI cell shown in the solid line and the homogeneous liquid crystal cell circles.

FIG. 6A is a schematic of a high transmittance and wide viewing angle LCD structure according to the present invention wherein uniaxial films are used as compensation film.

FIG. 6B shows a simulated iso-contrast plot for the structure shown in FIG. 6A with a positive dielectric anisotropic liquid crystal cell compensated by two uniaxial films.

FIG. 6C shows a simulated iso-contrast plot for the structure shown in FIG. 6A with a negative dielectric anisotropic liquid crystal cell compensated by two uniaxial films.

FIG. 7A is a schematic of high transmittance and wide viewing angle LCD structure in this invention where biaxial film is used as the compensation film.

FIG. 7B shows a simulated iso-contrast plot for the structure shown in FIG. 7A with positive dielectric anisotropic liquid crystal cells compensated by a biaxial film.

FIG. 7C shows a simulated iso-contrast plot for the structure shown in FIG. 7A with negative dielectric anisotropic liquid crystal cells compensated by a biaxial film.

FIG. 8A shows a simulated iso-brightness plot of a TN cell of the prior art.

FIG. 8B shows a simulated iso-brightness plot of the prior art IPS mode with transparent electrodes as shown in FIG. 1A.

FIG. 8C shows a simulated iso-brightness plot of the high transmittance and wide viewing angle LCD structure shown in FIG. 4A with a positive dielectric anisotropic liquid crystal material.

FIG. 8D is the simulated iso-brightness plot of the high transmittance and wide viewing angle LCD structure shown in FIG. 4A with a negative dielectric anisotropic liquid crystal material.

FIG. 8E shows the simulated iso-contrast plot of a prior art TN cell without compensation films.

FIG. 8F shows the simulated iso-contrast plot of prior art IPS mode with transparent electrodes as shown in FIG. 1B without compensation films.

FIG. 9A shows the simulated iso-contrast plot of the high transmittance and wide viewing angle LCD structure shown in FIG. 4A using a positive dielectric anisotropic liquid crystal material without compensation films.

FIG. 9B is the simulated iso-contrast plot of the high transmittance and wide viewing angle LCD structure shown in FIG. 4A using a negative dielectric anisotropic liquid crystal material without compensation films.

**DESCRIPTION OF THE PREFERRED EMBODIMENTS**

Before explaining the disclosed embodiments of the present invention in detail it is to be understood that the
The invention is not limited in its application to the details of the particular arrangements shown since the invention is capable of other embodiments. Also, the terminology used herein is for the purpose of description and not of limitation.

The following is a list of the reference numbers used in the drawings and the detailed specification to identify components:

<table>
<thead>
<tr>
<th>Reference Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>101a</td>
<td>first substrate</td>
</tr>
<tr>
<td>101b</td>
<td>second substrate</td>
</tr>
<tr>
<td>102</td>
<td>electrode</td>
</tr>
<tr>
<td>103</td>
<td>electrode</td>
</tr>
<tr>
<td>104</td>
<td>liquid crystal layer</td>
</tr>
<tr>
<td>201a</td>
<td>first substrate</td>
</tr>
<tr>
<td>201b</td>
<td>second substrate</td>
</tr>
<tr>
<td>202</td>
<td>common electrode</td>
</tr>
<tr>
<td>203</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>204</td>
<td>liquid crystal layer</td>
</tr>
<tr>
<td>301</td>
<td>common electrode</td>
</tr>
<tr>
<td>302</td>
<td>common electrode</td>
</tr>
<tr>
<td>303</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>304</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>305</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>306</td>
<td>common electrode</td>
</tr>
<tr>
<td>401</td>
<td>common electrode</td>
</tr>
<tr>
<td>402</td>
<td>common electrode</td>
</tr>
<tr>
<td>403</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>404</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>405</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>406</td>
<td>common electrode</td>
</tr>
<tr>
<td>407</td>
<td>insulating layer</td>
</tr>
<tr>
<td>410a</td>
<td>alignment layer</td>
</tr>
<tr>
<td>410b</td>
<td>alignment layer</td>
</tr>
<tr>
<td>501</td>
<td>common electrode</td>
</tr>
<tr>
<td>502</td>
<td>common electrode</td>
</tr>
<tr>
<td>503</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>504</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>505</td>
<td>pixel electrode</td>
</tr>
<tr>
<td>506</td>
<td>common electrode</td>
</tr>
<tr>
<td>511</td>
<td>compensation film</td>
</tr>
<tr>
<td>512</td>
<td>compensation film</td>
</tr>
<tr>
<td>513</td>
<td>biaxial layer</td>
</tr>
<tr>
<td>520a</td>
<td>first polarizer</td>
</tr>
<tr>
<td>520b</td>
<td>second polarizer</td>
</tr>
</tbody>
</table>

FIG. 2A shows the structure of a high transmittance and wide viewing angle LCD structure 300 with two electrode groups formed in the same layer. One group having common electrodes 301 and 302 and pixel electrode 303 functioning as a common electrode and the other group including common electrode 306 and pixel electrodes 304 and 305 functioning as a pixel electrode. An alignment layer 310a and 310b is formed on the interior surface of the first and second substrate 201a and 210b, respectively, and a liquid crystal layer 204 is sandwiched between the alignment layer 310a and 310b which are separated by a distance d, which is the thickness or cell gap of the liquid crystal cell.

The alignment layers 310a and 310b can be polyimide films and are interposed between the substrate 201a and the liquid crystal layer 204 and between the substrate 210b and the liquid crystal layer 204. A plurality of transparent common electrodes 301, 302, and 306 and a plurality of transparent pixel electrodes 303, 304, and 305 are formed on the substrate 201a in each pixel and a thin-film transistor (not shown) is formed on the substrate 201a in each pixel. Alternatively, the above-described structure further includes two linear polarizers (not shown) on the first and second substrates 201a and 210b wherein a polarizing axis of the first polarizer coincides with the rubbing direction of the liquid crystal layer or the direction perpendicular to the rubbing direction, and a polarizing axis of the second polarizer is perpendicular to the polarizing axis of the first polarizer.

According to FIG. 2A, common electrodes 301, 302, and pixel electrode 303 form a first group of electrodes with the pixel electrode 303 is located between the common electrodes 301 and 302. The second group of electrodes includes pixel electrodes 304 and 305 and common electrode 306 with common electrode 306 located between the pixel electrodes 304 and 305. The first and second groups of electrodes are formed on the same layer and are separated by a selected distance. The width of each electrode and the distances in-between the electrodes are selected such that the liquid crystal directors in the regions overlaid by each of the first and second electrode group are driven by substantial fringe electric fields and the liquid crystal directors in the regions overlaid by the electrode groups are driven by substantially horizontal electric fields.

To achieve the required electric field profile, the distances between the center common or pixel electrode to the two adjacent pixel or common electrodes are less than the thickness of the liquid crystal layer. As shown in FIG. 2A, the distance L1 between the common electrode 301 and the pixel electrode 303 is less than the thickness of the liquid crystal layer d. The distance L2 between the pixel electrode 303 and the common electrode 302, the distance L3 between pixel electrode 304 and common electrode 306, and the distance L4 between common electrode 306 and pixel electrode 305 are also less than the thickness d of the liquid crystal layer 204. As shown in FIG. 2A, the distance L5 between adjacent electrodes 302 and 304 from the two different electrode groups is greater than the cell gap d of the liquid crystal layer 204.

The liquid crystal layer 204 in this embodiment is a positive liquid crystal material with dielectric anisotropy ΔE greater than 0. is used. For the cell with positive liquid crystal materials, the preferred product of the thickness d of the liquid crystal layer and the refractive index anisotropy Δn of the liquid crystal material is within a range of approximately 0.25 to approximately 0.75 micrometers. The widths W1, W2, W4, and W5 of the electrodes 301, 302, 304, and 305 are not necessarily equal, but are preferred to have a ratio of...
approximately 0.1 to approximately 0.95 with respect to the cell gap \(d\) of the liquid crystal layer 204. Similarly, the widths \(W_3\) and \(W_6\) of the center electrodes 303 and 306 are not necessarily to be equal but are preferred to have a ratio of approximately 0.1 to approximately 0.75 micrometers with respect to the cell gap \(d\) of the liquid crystal layer. The distances \(L_{1.1}, L_{1.2}, L_{1.3}\) and \(L_{1.4}\) between adjacent electrodes preferably have a ratio of approximately 0.1 to 0.95 micrometers with respect to the cell gap \(d\) of the liquid crystal layer. The distance \(L_5\) between the two adjacent electrodes from different groups preferably has a ratio of approximately 1.0 to approximately 3.0 micrometers with respect to the cell gap \(d\).

For the homogeneously aligned (or anti-parallel rubbing) positive liquid crystal cell in this embodiment, the preferred rubbing angle of the positive liquid crystal materials on the inner surfaces is between approximately 45 degrees to approximately 90 degrees from the x-axis in the x-y plane and the pretilt angle on the inner surfaces of the substrates 201a and 201b is set in a range of approximately 0.5 degrees to approximately 10 degrees.

FIG. 2b shows a graph of the simulation results of the structure shown in FIG. 2a using a homogeneously aligned positive anisotropic liquid crystal material. The transmittance is greatly enhanced compared to the conventional IPS LCDs using transparent electrodes as in FIG. 1b. This is because the electrode groups function like a continuous common or pixel electrode since the widths of the electrodes and distances between the electrodes are less than the cell gap. Furthermore, the distance \(L_5\) between the adjacent two groups of electrodes is larger than the liquid crystal cell gap \(d\). As a result, substantially horizontal electric fields fully twist the liquid crystal directors and the transmittance between the two groups of electrodes is high. The regions above the two electrode groups substantially fringe fields are produced and twist the liquid crystal directors with less tilt than the conventional IPS mode because the electrode distance between adjacent pixels is smaller than the cell gap \(d\), which results in good transmittance. Therefore, both fringe field and the horizontal field components perform simultaneously to attain a high transmittance throughout the cell.

In this embodiment, the transmittance achieved is approximately 42.3% out of the possible maximum 46.0% value under the selected crossed-polarizers configuration shown in FIG. 2b. This mode has approximately 92.0% of the maximum transmittance, while a conventional IPS has a transmittance of only approximately 75.0%. Therefore, the mode produced by this embodiment of the present invention improves the light efficiency by approximately 22.0% when compared to the light efficiency of the prior art IPS mode.

In an alternative embodiment, a homogeneously aligned, or anti-parallel rubbing, negative \(\Delta\varepsilon\) liquid crystal cell is used in structure shown in FIG. 2a. In this embodiment the same dimension ranges are used and the preferred rubbing angle is between approximately 0 degrees to approximately 45 degrees away from the x-axis in the x-y plane and a pretilt angle is selected to be in a range of approximately 0.5 degrees to approximately 10 degrees.

FIG. 2c shows the simulation results of resulting from structure shown in FIG. 2a using a homogeneously aligned negative liquid crystal material 204f. The resulting transmittance is approximately 44.4% out of the possible maximum 46.0% value under the crossed-polarizers and has approximately 96.0% of the maximum transmittance. The higher transmittance from a negative liquid crystal material over the positive liquid crystal is evident because there is relatively little tilt effect above the electrode surface regions in a negative liquid crystal cell.

To simplify fabrication of the liquid crystal display, it is also preferable that the common and pixel electrodes are formed on different layers and are separated by an insulating layer, as shown in FIG. 3a. A liquid crystal layer 204 is sandwiched by inner surfaces of the first and second substrates 201a and 201b such as glass; two alignment layers 410a and 410b separated by a selected distance \(d\), which is the thickness or cell gap of the liquid crystal cell. The alignment layers 410a and 410b can be polyimide films. The first and second alignment layers 410a and 410b are located between the first and second substrate 201a and 201b and the liquid crystal layer 204, respectively. A plurality of repetitive common electrodes 401, 402, and 406 and a plurality of repetitive pixel electrodes 403, 404, and 405 are formed on the first substrate 201a in each pixel area and the common electrode and the pixel electrodes are located in different layers, separated by an insulating layer 407. Pixel electrodes 403, 404, and 405 are formed on the first substrate 201a while the common electrodes 401, 402, and 406 are formed on the insulating layer 407. A thin-film transistor (not shown) is also formed on the first substrate 201a in each pixel area. The first and second substrates 201a and 201b are sandwiched between a first and a second linear polarizer (not illustrated here). The polarizing axis of the first polarizer coincides with the rubbing direction or the direction which is perpendicular to the rubbing direction, and a polarizing axis of the second polarizer is perpendicular to the polarizing axis of the first polarizer.

As shown in FIG. 3A, in each small repeating period in one pixel, common electrodes 401, 402 and pixel electrode 403 forms a first group of electrodes, where the pixel electrode 403 is between the common electrodes 401 and 402. The second group of electrodes includes pixel electrodes 404, 405 and common electrode 406 and the common electrode 406 is between the pixel electrodes 404 and 405. Like the previous embodiment shown in FIG. 2a, the liquid crystal directors in the regions overlying above each electrode group are driven by substantial fringe electric fields and the liquid crystal directors in the regions overlying between the electrode groups are driven by substantial horizontal electric fields to achieve high transmittance. The preferred rubbing angle range for the homogeneously aligned positive liquid crystal cell is approximately 45 degrees to approximately 90 degrees away from the x-axis in the x-y plane. And the pretilt angle is set in a range of around 0.5 degrees to 10 degrees.

In this embodiment, to obtain a substantial fringe electric field, the width of the pixel electrode 403 can be larger than the distance between the adjacent common electrodes 401 and 402 in the first group of electrodes. In the second group of electrodes, the distance \(L_3\) between the pixel electrodes 404 and 405 can be less than the width of the common electrode 406. In fact, pixel electrodes 404 and 405 can merge into one pixel electrode. However, the distance \(L_3\) between the first and second electrode groups is still required to be equal to or larger than the cell gap \(d\) of the liquid crystal layer. More specifically, it is preferred that the widths \(W_1\) and \(W_2\) of common electrodes 401 and 402 have a ratio between approximately 0.1 and approximately 0.95 in respect to the cell gap \(d\) of the liquid crystal layer and the width \(W_3\) of the pixel electrode 403 is preferred to have a ratio between approximately 0.1 and approximately 2.5 with respect to the liquid crystal layer cell gap \(d\). In the second group of electrodes; the widths \(W_4\) and \(W_5\) of the pixel electrodes 404 and 405 are preferred to have a ratio between approximately 0.1 and approximately 2.75 with respect to the liquid crystal layer cell gap \(d\) and the width \(W_6\) of the common electrode 406 is
preferred to have a ratio between approximately 0.1 and approximately 0.95 with regard to the cell gap d.

The distance L1 between the common electrodes 401 and 402 has a preferred ratio between approximately 0.1 and approximately 2.0 with respect to the cell gap and the distance L2 between pixel electrodes 404 and 405 has a preferred ratio of between approximately 0 and approximately 2.65 with respect to the liquid crystal layer cell gap. The distance L3 between the two groups of electrodes, is preferred to have a ratio between approximately 1.0 and approximately 3.0 with regard to cell gap d to generate a horizontal electric fields. To further determine the relative positions between the two electrode groups, the distance L4 between common electrodes 403 and 404 is preferred to have a ratio of between approximately 1.1 and approximately 5.65 with respect to the cell gap.

The liquid crystal layer is a homogeneously aligned positive liquid crystal. The rubbing angle is approximately 45 degrees to approximately 90 degrees away from the x-axis in the x-y plane and the preferred pretilt angle is in a range of approximately 0.5 degrees to approximately 10 degrees. FIG. 3b is a graph showing the simulation results of the structure shown in FIG. 3a when the liquid crystal layer is a positive liquid crystal material. As shown, the transmittance is also greatly enhanced in comparison to the conventional in-plane switching LCD using transparent electrodes. The achieved transmittance is approximately 42.0% out of the possible maximum 46.0% which is approximately 91.0% of the maximum transmittance.

Alternatively, homogeneously aligned negative liquid crystal material is used as the liquid crystal layer in the structure shown in FIG. 3c with same dimension ranges as described above with the positive liquid crystal material. The preferred rubbing angle is in this alternative embodiment is approximately 0 degrees to approximately 45 degrees from the x-axis in the x-y plane and the preferred pretilt angle is between approximately 0.5 degrees and approximately 10 degrees. FIG. 3c shows a graph of the transmittance curve corresponding to the structure illustrated in FIG. 3a with a negative liquid crystal material. The transmittance is approximately 44.5% under the selected crossed-polarizers which is approximately 96.6% of the maximum transmittance.

To further ease the fabrication of the liquid crystal device of the present invention, an alternative structure is shown in FIG. 4a. As in all the previous embodiments, alignment layers 410a and 410b are formed on the first and second substrate 201a and 201b and the liquid crystal layer 204 is sandwiched between the alignment layers 410a and 410b separated by a distance d, which is the cell gap. Two groups of transparent electrodes are formed on the substrate 201a in each pixel. The pixel electrode 503 and the common electrode 506 are located in the same layer; and the common electrodes 501 and 502 and the pixel electrodes 504 and 505 are located in another layer and the two layers are separated by the insulating layer 507, as shown in FIGS. 3a and 4a.

The difference between the structures shown in FIGS. 3a and 4a, is the width of the pixels and their placement. For example, in the first group of pixels shown in FIGS. 3a and 4a includes a pixel electrode 403 and 503, respectively, on the first substrate with completely different width W3.

To achieve above-described field distribution, a homogeneously aligned positive liquid crystal material is used. For a cell with positive liquid crystal materials, the preferred product of the cell gap d and the refractive anisotropy of the liquid crystal is between approximately 0.25 and approximately 0.75 micrometers. The widths W1, W2, W4, and W5 of the electrodes 501, 502, 504, and 505 formed on the insulating layer 407 are preferred to have a ratio between approximately 0.1 and approximately 0.95 with respect to the cell gap d, however, the widths W1, W2, W4, and W are not required to be equal. The widths W3 and W6 of the electrodes 503 and 506 on the first substrate 201a are preferred to have a ratio between approximately 0.1 and approximately 4.4 with respect to the cell gap d, however the widths W3 and W6 are not required to be equal.

The distances L1 and L2 between common electrodes 501 and 502 and pixel electrodes 504 and 505, respectively, have a preferred ratio of approximately 0.1 to approximately 2.0 with respect to the cell gap. The distance L3 between the two adjacent common electrode 502 and pixel electrode 504 on the insulating layer is preferred to have a ratio of approximately 1.0 to approximately 3.0 with respect to the cell gap d. The distance L4 between the two adjacent electrodes 503 and 506 in the other layer is preferred to have a ratio of 1.0 to 8.7 with respect to the cell gap d. Furthermore, as shown in FIG. 4a, to determine the relative position between the electrodes in two different layers, the distances L5 and L6 between the common electrode 502 on the insulating layer and the common electrode 506 on the first substrate 201a and between the pixel electrode 503 on the first substrate 201a and the pixel electrode 504 on the insulating layer 407 have a preferred ratio of approximately 1.0 to approximately 5.85 and approximately 1.0 and approximately 5.85 with respect to the cell gap d, respectively.

FIG. 4b shows a graph of the simulation results for the structure shown in FIG. 4a with positive liquid crystal material, where the L3 value is larger than the L4 value. As shown by the graph, the transmittance is enhanced compared to the transmittance of a conventional IPS LCDs using transparent electrodes and the transmittance is as high as 92.0% of the transmittance of a conventional TN cell.

Alternatively, a homogeneously aligned, or anti-parallel rubbing, negative liquid crystal material is used in the structure shown in FIG. 4c which is the plot for the simulated transmittance versus position. The device has the same dimension ranges for the product of the cell gap d and the refractive index anisotropy of the liquid crystal, the same widths of each common and pixel electrodes and the same distances in between the two groups of electrodes. A difference between the structures shown in FIGS. 4a and 4c is that the distance L3 in FIG. 4c is less than the distance L4. However, the preferred rubbing angle is approximately 0 degrees to approximately 45 degrees away from the x-axis in the x-y plane and the pretilt angle is set in a range of approximately 0.5 degrees to approximately 10 degrees. Using this alternative configuration, the transmittance reaches as high as 98.0% of a conventional TN cell.

In the previously described LCD devices, the liquid crystal layers are homogeneously aligned (or anti-parallel rubbing). However, to reduce the color shift of these devices, the PI cell, or parallel rubbed liquid crystal display devices as described in co-pending U.S. patent application Ser. No. 10/639,985, filed on Aug. 13, 2003, assigned to the same assignee as the present invention, can be substituted.

In this embodiment, a PI cell with positive liquid crystal is applied to the structure in shown FIG. 4a with same dimension ranges for the product of the cell gap d and the refractive index anisotropy of the liquid crystal, the same widths of each common and pixel electrodes, the same distances therebetween, and the same rubbing angle and pretilt angle.

FIG. 5a shows the simulated color shift performances of a homogeneous liquid crystal cell and a PI cell applied in the
structure shown in FIG. 4a when a positive liquid crystal materials is used. The dots in FIG. 5a represent the color shift of the homogeneous cell, and the solid line in FIG. 5a represents the color shift of the PI cell. Because the PI cell inherently has a more symmetrical liquid crystal director distribution, as shown in FIG. 5a, the color shift of the PI cell is smaller than that of the homogeneous cell.

Alternatively, a PI cell with negative liquid crystal is used in the structure shown in FIG. 4a with a preferred rubbing angle between approximately 0 degrees and approximately 45 degrees away from the x-axis in the x-y plane and the top layer surfaces of the liquid crystal layer are parallel-rubbed with a preferred pretilt angle of approximately 0.5 to approximately 10 degrees. FIG. 5b shows the simulated color shift performance of a homogeneous liquid crystal cell and a PI cell used in the structure as FIG. 4a, with negative liquid crystal materials. The dots represent the color shift of the homogeneous cell, and the solid line represents the color shift of the PI cell. The PI cell shows less color shift than the homogeneous cell.

As shown in FIGS. 5a and 5b, introduction of a PI cell with either positive or negative materials into the structure in FIG. 4a reduces the color shift. The use of PI cell is not limited to use with the structure shown in FIG. 4a, the PI cell can be substituted in the structures in which initial liquid crystal directors are aligned parallel to the substrates and the horizontal fields mainly drive the liquid crystal directors when voltages are applied.

By introducing additional compensation films, such as uniaxial films to above-described structures, the viewing angle is greatly expanded. The above-described LCD structures have approximately the same initial liquid crystal director distributions, where in their voltage-off states the liquid crystal directors are parallel to the substrates. Hence similar film compensation schemes can be applied. For illustration, two uniaxial films as described in J. Chen, Optimum Film Compensation Modes for TN and VA LCDs, SID (1998), p. 315, are here used to compensate the dark state of the structure as FIG. 4a when positive liquid crystal material is used.

As shown in FIG. 6a, layers 520a and 520b are the first polarizer (previously described and not shown) and the second polarizer, respectively, wherein the absorption axis of the first polarizer 520a is coincided with the rubbing direction of liquid crystal layer 204, and the absorption axis of the second polarizer 520b is perpendicular to the absorption axis of the first polarizer 520a. Compensation films 511 and 512 are formed between the second polarizer 520b and the second substrate 500b. In this embodiment, compensation film 511 is a positive c-plate wherein the product of the thickness of the film and its refractive index anisotropy is set in a range of approximately 0.085 to approximately 0.095 micrometers. Compensation film 512 is a positive a-plate with a product of its thickness and its refractive index anisotropy in a range of approximately 0.13 micrometers; having an optical axis that coincides with the rubbing direction of the liquid crystal layer.

FIG. 6b shows the simulated iso-contrast plot for the film compensated liquid crystal display shown in FIG. 6a with positive material. As shown, the contrast ratio is greatly enhanced to approximately greater than 120:1 at a viewing cone greater than approximately 85 degrees at all azimuthal angles.

Alternatively, the compensation layers 511 and 512 are located between the bottom substrate 201a and the first polarizer 520a, wherein the absorption axis of the second polarizer 520b and the optical axis of the positive a plate 512 are set coincided with the rubbing direction of liquid crystal layer 204.

Alternatively, the liquid crystal layer in the structure shown in FIG. 6a is a negative liquid crystal material. When negative material is used, the first compensation film 511 is a positive c-plate with a product of its thickness and its refractive index anisotropy in a range of approximately 0.085 to approximately 0.095 micrometers. The second compensation film 512 is a positive a-plate with a product of its thickness and its refractive index anisotropy in a range of approximately 0.13 micrometers; and the optical axis coincides with the rubbing direction of the negative liquid crystal layer.

FIG. 6c shows the simulated iso-contrast plot for the film compensated liquid crystal display shown in FIG. 6a with negative material used. The contrast ratio is greatly enhanced to approximately greater than 120:1 at a viewing cone greater than approximately 85 degrees at all azimuthal angles.

As previously described in regard to positive liquid crystal material, when the liquid crystal layer is negative, the compensation films 511 and 512 are located between the bottom substrate 201a and the first polarizer 520a, wherein the absorption axis of the second polarizer 520b and the optical axis of the positive a plate 512 are set coincided with the rubbing direction of liquid crystal layer 204.

The introduction of uniaxial compensation films into the structure in FIG. 4a with both positive and negative materials enhances the viewing angle. However, the uniaxial compensation films can also be used with the structures shown in FIGS. 2a and 3a with both positive and negative liquid crystal materials, in which initial liquid crystal directors are aligned parallel to the substrates in their voltage-off states.

Alternatively, the uniaxial compensation film 511 and 512 shown in FIG. 6a is replaced with a single biaxial compensation film 513 between the second substrate 201b and the second polarizer 520b as shown in FIG. 7a. The biaxial film, wherein nx, ny and nz are not equal, is used to compensate the dark state when a positive liquid crystal material is used as described in Y. Saitoh et al., Optimum Film Compensation of Viewing Angle of Contrast in In-Plane Switching-Mode Liquid Crystal Display, Jpn. J. Appl. Phys., Vol. 37 (1998), pp. 4822-4828. The preferred rubbing angle of the positive liquid crystal material is in a range of approximately 45 degrees to approximately 90 degrees.

To reduce the light leakage at large viewing angles in the voltage-off state while not interfering with the normal incidence, the nz axis is set perpendicular to the x-y plane and one of the other nx or ny axis is coincides with the rubbing direction of the liquid crystal layer. The preferred product of the film thickness and the difference between nx and ny is in a range of approximately 0.26 micrometers to approximately 0.28 micrometers. The preferred product of thickness and refractive index anisotropy is set in a range of approximately 0.45 to approximately 0.55. FIG. 7b shows the simulated contrast ratio of the above compensated structure with positive materials. The greater than 200:1 contrast ratio is enhanced to within approximately an 80-degree viewing cone.

Alternatively, the biaxial compensation layer 513 is located between the first substrate 201a and the first polarizer 520a, wherein the nz axis is set perpendicular to the x-y plane and one of the nx or ny axis coincides with the rubbing direction of the liquid crystal layer.

Although the biaxial compensation layer has been described for use with a structure having positive liquid crystal material, negative material may be substituted to compensate the dark state of the structure as FIG. 7a with negative
liquid crystal material having a preferred rubbing angle in a range of approximately 0 degrees to approximately 45 degrees. The nz axis of the biaxial film 513 is set perpendicular to the x-y plane and one of the nx or ny axis coincides with the rubbing direction of the negative liquid crystal layer. The preferred product of the film thickness and the difference as between nx and ny is in a range of approximately 0.26 micrometers to approximately 0.28 micrometers and the preferred (nx-nz)/(nx-ny) is approximately 0.45 to approximately 0.55.

FIG. 7c shows the simulated contrast ratio of the above compensated structure with negative material. The greater than 200:1 contrast ratio is enhanced to within an 80-degree viewing cone for this negative liquid crystal cell. As previously described in regard to the structure having positive liquid crystal material, the biaxial compensation layer 513 is alternatively located between the bottom substrate 201a and the first polarizer 520a, wherein its nz axis is set perpendicular to the x-y plane and its nx (or ny) axis is set in coincidence with the rubbing direction of the liquid crystal layer.

As previously described in regard to the use of two uniaxial compensation films 511 and 512, the substitution with biaxial film 513 also applied to the structures shown in FIGS. 2a and 3a to enhance the viewing angle with both positive and negative liquid crystals in which initial liquid crystal directors are aligned parallel to the substrates in their voltage-off states.

To show the unique brightness performance of the present invention, a series of simulated iso-brightness curves were plotted. The simulated iso-brightness curves are shown in FIGS. 8a to 8d for the conventional TN cell, the conventional IPS cell of FIG. 1b, the LCD cell shown in FIG. 4a with positive liquid crystal material and with negative liquid crystal material, respectively. The TN cell simulated iso-brightness curves shown in FIG. 8a has the highest brightness of approximately 100% at the center with a greater than 90% transmittance curve within a cone of approximately 35 degrees at all azimuthal angles from approximately 0 degrees to approximately 360 degrees. The TN cell also has a greater than 75% transmittance curve within a cone of approximately 45 degrees at all azimuthal angles. The conventional IPS mode cell with transparent electrodes iso-brightness plot shown in FIG. 8b has the most brightness performance. As shown in FIG. 8b, the maximum transmittance for the IPS cell at the center is approximately 75% of the TN cell and is very narrow.

Unlike the IPS cell, the structures of the present invention have greatly enhanced the iso-brightness performance as shown in FIGS. 8c and 8d. The structures with positive liquid crystal material have expanded the greater than 75% transmittance cone to approximately 40 degrees at all azimuthal angles as shown in FIG. 8c. With negative liquid crystal materials have expanded the greater than 75% transmittance cone to approximately 50 degrees at all azimuthal angles as shown in FIG. 8d. Moreover, the greater than 90% transmittance cone in FIG. 8d (negative material) even reaches a viewing cone of approximately 38 degrees at all azimuthal angles, which is a wider viewing cone than achieved with the TN cell.

FIGS. 9a through 9d show the iso-contrast plot of the conventional TN cell, the conventional IPS cell in FIG. 1b, the LCD cell of the present invention with positive liquid crystal material, and with negative liquid crystal material respectively without use of compensation films. Since the liquid crystal directors in a TN cell are aligned parallel to one substrate but nearly aligned perpendicularly to the other substrate, the contrast ratio are strongly angular dependent as shown in FIG. 9a. The butterfly shaped iso-contrast plot shows a very narrow viewing angle. FIG. 9b shows the iso-contrast plot for a conventional IPS cell without compensation films. Because the IPS cell directors are initially aligned parallel to the substrate, the viewing angle is less azimuthally dependent and wider.

The liquid crystal directors of the present invention in their dark states or voltage-off states, are also initially aligned parallel to the substrates, i.e., the viewing angle is also less azimuthally dependent. However, the liquid crystal display devices of the present invention achieve a higher transmittance than the conventional IPS mode. Similarly, the liquid crystal display devices of the present invention have a wider viewing angle, especially in the cone close to the center as shown in the iso-contrast plots shown in FIGS. 9c and 9d.

In summary, the structures of the present invention attain a comparable iso-brightness performance as the conventional TN cell. Both positive and negative liquid crystal material are used to reach a higher than 90% transmittance of a conventional TN cell. At the same time these LCD devices of the present invention have a wide viewing angle that is better than the viewing angle of the conventional IPS modes.

While the invention has been described, disclosed, illustrated and shown in various terms of certain embodiments or modifications which it has presumed in practice, the scope of the invention is not intended to be, nor should it be deemed to be, limited thereby and such other modifications or embodiments as may be suggested by the teachings herein are particularly reserved especially as they fall within the breadth and scope of the claims here appended.

We claim:

1. A liquid crystal display device comprising:
   - a first substrate and a second substrate having plural pixels defined between the first and the second substrate;
   - a first and a second polarizing layer on an exterior surface of the first and the second substrate, respectively;
   - a first and a second alignment layer on an inner surface of the first and the second substrate, respectively;
   - a liquid crystal layer sandwiched between the first and second alignment layer, the liquid crystal layer treated so that liquid crystal molecules are aligned parallel to the surfaces of the first and second substrate in the absence of applied electric fields;
   - an insulating layer between the one of the first and second alignment layers and the corresponding one of the first and the second substrate on which the first and the second group of electrodes are formed; and
   - each of the plural pixels comprising:
     - a first group of electrodes including a first and a second common electrode formed on the insulating layer and a first pixel electrode formed on the one of the first and the second substrate below the first and the second common electrode, wherein the first and the second common electrodes have widths in the range of approximately 0.1 to approximately 0.95 times of the thickness of the liquid crystal layer, the first pixel electrode having a width larger than approximately 0.1 of the thickness of the liquid crystal layer, and the distance between the first pixel electrode and each of the first and the second common electrodes is less than a liquid crystal layer cell gap; and
     - a second group of electrodes including a second and a third pixel electrode formed on the one of the first and second substrate and a third common electrode formed on the insulating layer above the second and the third pixel electrode, the widths of the second and the third pixel electrode and the third common electrode are less than a cell gap of the liquid crystal layer,
wherein the horizontal distance between the first and the second group of electrodes is greater than or equal to the thickness of the liquid crystal layer; and a thin-film transistor formed on the one of the first and second substrate having the first and second group of electrodes formed thereon; and wherein a substantially fringing electric field containing both horizontal and vertical electric field components is generated within the regions above and below the first and second group of electrodes and a substantially horizontal electric field is generated within the regions of the liquid crystal layer between the first and second group of electrodes.

2. The liquid crystal display device of claim 1 wherein the second and third pixel electrodes have a width larger than approximately 0.1 the thickness of the liquid crystal layer, the third common electrode has a width in the range of 0.1 to approximately 0.95 times the thickness of the liquid crystal layer, and the distance between the third common electrode and each of the second and third pixel electrodes is less than a cell gap of the liquid crystal layer.

3. The liquid crystal display of claim 1 wherein the liquid crystal layer comprises:
   a positive liquid crystal material with a dielectric anisotropy greater than zero and has a rubbing direction between approximately 45 degrees and approximately 90 degrees away from the x-axis, wherein the product of the thickness of the liquid crystal layer and a refractive index anisotropy of the liquid crystal layer is within a range of approximately 0.25 and approximately 0.75 micrometers.

4. The liquid crystal display of claim 1 wherein the liquid crystal layer comprises:
   a negative liquid crystal material with a dielectric anisotropy greater than zero and has a rubbing direction between approximately 0 degrees and approximately 45 degrees away from the x-axis, wherein the product of the thickness of the liquid crystal layer and a refractive index anisotropy of the liquid crystal layer is within a range of approximately 0.25 and approximately 0.75 micrometers.

5. The liquid crystal display of claim 1 wherein the width of the first pixel electrode and the third common electrode are each less than the distance between the first and second common electrode and the second and third pixel electrode, respectively.

6. The liquid crystal display of claim 1 wherein the width of the first pixel electrode and the third common electrode are each greater than the distance between the first and second common electrode and the second and third pixel electrode, respectively.

7. The liquid crystal device of claim 6, further comprising:
   at least one compensation film located between one of the first and second polarizing layer and the corresponding one of the first and second substrate to improve the viewing angle of the liquid crystal display.

8. The liquid crystal display of claim 7 wherein the at least one compensation film comprises:
   a first and a second uniaxial film to compensate the dark state of the liquid crystal display device.

9. The liquid crystal display of claim 7 wherein the first and a second uniaxial film comprises:
   a positive a-plate film having a product of its thickness and its refractive index anisotropy is between approximately 0.085 to approximately 0.095 micrometers; and

10. The liquid crystal display of claim 7 wherein the at least one compensation film comprises:
    a biaxial film to reduce the light leakage at large viewing angles in voltage off state.

11. The liquid crystal display device of claim 1 wherein the liquid crystal molecules contacting the first alignment layer on the first substrate and the second alignment layer on the second substrate form a pretilt angle with respect to the surfaces of the first and the second substrates in a range of approximately 0.5 degree to approximately 10 degrees.

12. The liquid crystal display device of claim 1 wherein the first and second alignment layer rubbed anti-parallel to each other.

13. The liquid crystal display device of claim 1 wherein the first and second alignment layer are rubbed parallel to each other.

14. The liquid crystal display device of claim 1 wherein the first pixel electrode overlaps the first and second common electrodes and the third common electrode overlaps the second and third pixel electrodes.

15. The liquid crystal display device of claim 1 wherein the distance between the first and second common electrode has a ratio between approximately 0.1 and approximately 2.0 with respect to the cell gap of the liquid crystal layer and the distance between the second and third pixel electrode has a ratio of between approximately 0.0 and approximately 2.65 with respect to the cell gap of the liquid crystal layer.

16. The liquid crystal display device of claim 1 wherein the horizontal spacing between the first and second group of electrodes is a ratio between approximately 1.0 and approximately 3.0 with regard to the thickness of the liquid crystal layer to generate a horizontal electric field.

17. The liquid crystal display device of claim 1 wherein the distance between the second common electrode and the adjacent third common electrode has a ratio of between approximately 1.1 and approximately 5.65 with respect to the cell gap and the distance between the first pixel electrode and the adjacent second pixel electrode has a ratio between approximately 1.1 and approximately 5.85 with respect to the cell gap.

18. A liquid crystal display device comprising:
   a first substrate and a second substrate having plural pixels defined between the first and the second substrate; a first and a second polarizing layer on an exterior surface of the first and the second substrate, respectively; a first and a second alignment layer on an inner surface of the first and the second substrate, respectively; a liquid crystal layer sandwiched between the first and second alignment layer, the liquid crystal layer treated so that liquid crystal molecules are aligned parallel to the surfaces of the first and second substrate in the absence of applied electric fields; an insulating layer between the one of the first and second alignment layers and the corresponding one of the first and second substrate on which the first and the second group of electrodes are formed; and each of the plural pixels comprising:
   a first group of electrodes including a first and second common electrode formed on the insulating layer and a first pixel electrode formed on the one of the first and the second substrate below the first and the second common electrode, wherein the width of the first and
second common electrode and the pixel electrode is less than a cell gap of the liquid crystal layer; and a second group of electrodes including a second and third pixel electrode formed on the one of the first and second substrate and a third common electrode formed on the insulating layer above the second and third common electrode, the width of the second and third pixel electrode and the third common electrode is less than a cell gap of the liquid crystal layer, wherein the horizontal spacing between the first and second group of electrodes is a ratio between approximately 1.0 and approximately 3.0 with regard to the thickness of the liquid crystal layer to generate a horizontal electric field; and a thin-film transistor formed on the one of the first and second substrate having the first and second group of electrodes formed thereon; and wherein a substantially fringing electric field containing both horizontal and vertical electric field components is generated within a region above and below the first and second group of electrodes and a substantially horizontal electric field is generated within the regions of the liquid crystal layer between the first and second group of electrodes.

19. A liquid crystal display device comprising: a first substrate and a second substrate having plural pixels defined between the first and the second substrate; a first and a second polarizing layer on an exterior surface of the first and the second substrate, respectively; a first and a second alignment layer on an inner surface of the first and the second substrate, respectively; a liquid crystal layer sandwiched between the first and second alignment layer, the liquid crystal layer treated so that liquid crystal molecules are aligned parallel to the surfaces of the first and second substrate in the absence of applied electric fields; an insulating layer between the one of the first and second alignment layers and the corresponding one of the first and the second substrate on which the first and the second group of electrodes are formed; and each of the plural pixels comprising: a first group of electrodes including a first and second common electrode formed on the insulating layer and a first pixel electrode formed on the one of the first and the second substrate below the first and the second common electrode, wherein the width of the first and second common electrode and the pixel electrode is less than a cell gap of the liquid crystal layer; and a second group of electrodes including a second and third pixel electrode formed on the one of the first and second substrate and a third common electrode formed on the insulating layer above the second and third common electrode, the width of the second and third pixel electrode and the third common electrode is less than a cell gap of the liquid crystal layer, wherein the horizontal distance between the first and second group of electrodes is greater than or equal to the thickness of the liquid crystal layer and the distance between the second common electrode and the adjacent third common electrode has a ratio of between approximately 1.1 and approximately 5.65 with respect to the cell gap and the distance between the first pixel electrode and the adjacent second pixel electrode has a ratio between approximately 1.1 and approximately 5.85 with respect to the cell gap; and a thin-film transistor formed on the one of the first and second substrate having the first and second group of electrodes formed thereon; and wherein a substantially fringing electric field containing both horizontal and vertical electric field components is generated within a region above and below the first and second group of electrodes and a substantially horizontal electric field is generated within the regions of the liquid crystal layer between the first and second group of electrodes.